

**REMARKS**

Claim 1 calls for generating an intermediate representation of a source program where the source program includes one or more instructions for processing data in a bit field within a data structure. The claim further calls for modifying that intermediate representation "to more efficiently execute the one or more instructions for processing the bit field data." The latest office action suggests that bit fields are taught and that somehow the source program includes instructions for processing data in the bit field. If we accept that for argument purposes, we are left with the question -- where is the modifying that intermediate representation to more efficiently execute the one or more instructions for processing the bit field data?

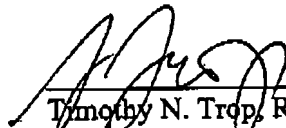
The asserted support for this limitation consists entirely of the clause "... then modifies the IR to produce a final version of the IR that corresponds to instructions in the second instruction set." See Tye at column 63, lines 20-22. But this says nothing about the bit field data whatsoever. Not only does it not even mention the bit field data in particular, but it in no way suggests that the intermediate representation is modified "to more efficiently execute the one or more instructions for processing the bit field data." Instead, it is indicated that the intermediate representation is modified to produce a final version "that corresponds to instructions in the second instruction set."

There is no basis for the conclusion drawn in the office action that this language somehow suggests causing the intermediate representation to be modified to more efficiently execute the one or more instructions for processing the bit field data. The bit field data is not even mentioned with respect to the modification. This is an example of the reference teaching the general, not the specific. Simply teaching modification does not teach the claimed modification.

Therefore, reconsideration is respectfully requested.

Respectfully submitted,

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